

SCTP1900Nxx

Phase control press-pack thyristor

FEATURES

- -All difussed design
- -High current capabilities
- -High surge current capabilities
- -High rated voltage
- -High dv/dt
- -Low gate current
- -Dynamic gate
- -Low thermal impedance
- -Compact size and small weight

APPLICATION

- -High Power drives -DC motor control
- -High voltage power cuplplies
- -Resistance welding
- -resistance welding



Photo non-contractual

TECHNICAL SPECIFICATION

Electrical properties

Parameter		Test conditions	Value
Repetitive reverse voltage	V _{RRM}	SCTP1900N12	1200 V
		SCTP1900N16	1600 V
Reverse current	I _{RRM}	Tj _{max}	100 mA
Average on-state current	I _{AV}	T _C =70°C	1900 A
R.M.S. Forward current	I _{RMS}		3000 A
Surge current	I _{TSM}	10ms, Tj _{max,} 0.8V _{RRM}	30000 A
l ² t value	l ² t		4500x10 ³ A ² s
On-state voltage max.	V _T	I _{TM} =1500A, Tj _{max}	1,05 V
Treshold voltage	Vo		0,74 V
Slope resistance	ro		0,089 mOhm
Latching current	١L	Tj=25°C, V _D =12V	1000 mA
Holding current	I _H	Tj=25°C, V _D =12V	250 mA
Circuit conmutated turno-off time (typical)	tq	Tj=125°C, I _{TM} =250A, di _R /dt=25A/µs, V _D =0,67V _{DRM} , V _{RM} =100V.	100 µs
Turn-on time (typical)	t _{on}	I _{TM} =100A, V _{DM} =100V	12 µs
Rate of change of current	di/dt	Tj=125°C, I _{TM} =3I _{AV} , V _D =0,67V _{DRM} , f=50Hz, I _{GM} =1A, di _G /dt=1A/μs	200 A/µs
Rate of rise of voltage	dv/dt	Tj=125°C, V _D =0,67V _{DRM}	1000 V/µs
Trigger gate current	l _G	Tj=25°C, V _D =12V	200 mA
Gate trigger voltage	V _G	Tj=25°C, V _D =12V	3 V

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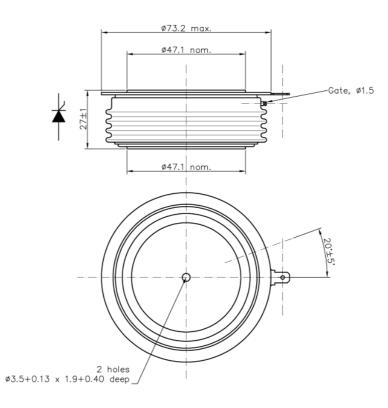
TP1900N_i

Thermal properties

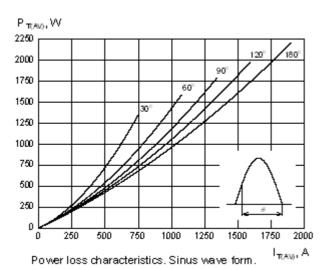
Parameter		Test conditions	Value
Max. operating junction temperature	Tj _{max}		125 °C
Thermal resistance junction-capsule		DC	0,021 °C/W.
	RTH _{i-c}	180° sin	0,021 °C/W.
	IXIIIj-c	120° sin	0,0215 °C/W.
		60° sin	0,0225 °C/W.
Thermal resistance capsule-heatsink	RTH_{c-hs}		0,01°C/W.
Storage temperature	T _{stg}		-40+125°C

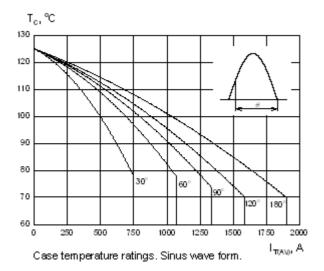
Mechanical properties

Parameter		Value
Weight	М	480 g
Clamping force	m	22,525 kN

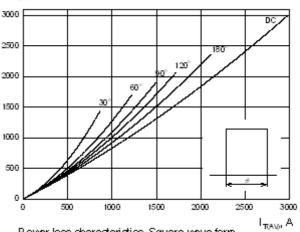


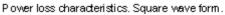


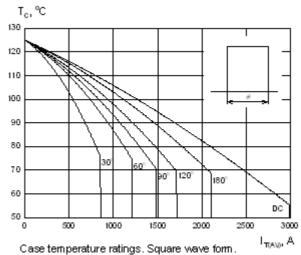


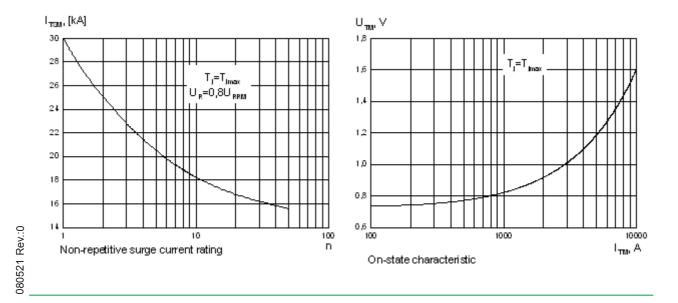










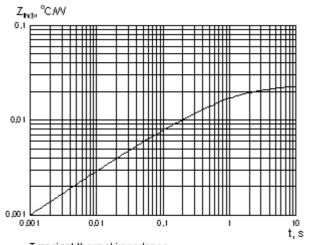


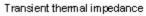
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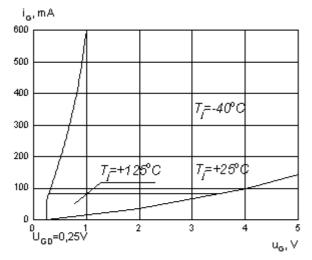
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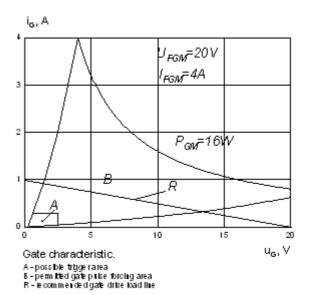
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Gate characteristic. Possible trigger area.





Cost Effective Products

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